



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

ATTY DOCKET NO.: 5649-927dv
DATE: November 12, 2003

**UTILITY PATENT APPLICATION TRANSMITTAL LETTER
AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))**



Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing under 37 CFR § 1.53(b) is:

- ☐ a patent application
☐ a Continuation ☒ a Divisional ☐ a Continuation-in-Part (CIP) Application of prior application no.: 10/052,721 ; filed January 18, 2002 , the disclosure of which is incorporated herein by reference in its entirety.
☐ and claims the benefit of Provisional Application No. _____; filed _____.

Inventor(s) or Application Identifier:

Hyoung-Joon Kim
Young-Wook Park
Byeong-Yun Nam

Entitled: **METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES PROVIDING IMPROVED SHORT PREVENTION**

Myers Bigel Sibley & Sajovec, P.A.
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Customer No. 20792

Enclosed are:

1. ☒ Application Transmittal Letter and Fee Transmittal Form (*A duplicate is enclosed for fee processing*)
2. ☒ **22** pages of Specification (including **12** claims)
3. ☒ **19** sheets of Formal Drawings (35 USC 113)
4. ☒ Oath or Declaration
 - a. ☐ newly executed (*original or copy*)
 - b. ☒ copy from prior application (37 CFR 1.63(d) (*for continuation/divisional*) [Note Box 6 Below]
 - c. ☐ **DELETION OF INVENTOR(S)** (*Signed statement deleting inventor(s) named in the prior application*)
5. ☐ Application Data Sheet. See 37 CFR 1.76
6. ☒ Incorporation By Reference (*useable if box 4b is checked*)

The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under

Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.


7. ☐ Microfiche Computer Program (*Appendix*)
8. ☐ Assignment papers (*cover sheet(s) and document(s)*)
9. ☐ Applicant claims small entity status. See 37 CFR § 1.27.
10. ☒ Information Disclosure Statement, PTO-1449, and 2 references cited
11. ☐ Preliminary Amendment (*Please enter all claim amendments prior to calculating the filing fee.*)
12. ☐ English Translation Document
13. ☐ Certified Copy of Application No. ; Filed
14. ☐ Sequence Listing/ Sequence Listing Diskette
 - a. ☐ computer readable copy
 - b. ☐ paper copy
 - c. ☐ statement in support
15. ☐ An Associate Power of Attorney
16. ☒ Return Receipt Postcard (MPEP 503) (*Should be specifically itemized*)
17. ☐ Deletion of Inventor(s) (Signed statement attached deleting inventor(s) name in the prior application. See 37 CFR 1.63(d)(2) and 1.33(b).
18. ☐ Nonpublication Request under 35 U.S.C. 122(b)(2)(B)(i). Applicant must attach form.
19. ☐ Other:

The fee has been calculated as shown below:

	Column 1 No. Filed	Column 2 No. Extra	Small Entity Rate Fee	Large Entity Rate Fee
BASIC FEE			\$385.00	\$ 770.00
TOTAL CLAIMS	12 - 20 =	0	x 09 = \$	x 18 = \$
INDEP CLAIMS	3 - 3 =	0	x 43 = \$	x 86 = \$
<input type="checkbox"/> MULTIPLE Dependent Claims Presented			+ 145 = \$	+ 290 = \$
If the difference in Col. 1 is less than zero, Enter "0" in Col. 2			Total \$	Total \$770.00

- ☒ A check in the amount of \$ 770.00 to cover the filing fee is enclosed.
- ☐ A check in the amount of \$ is enclosed to cover the filing fee, PLUS the Assignment Recordation fee (\$40.00).
- ☐ Please charge my Deposit Account No. 50-0220 in the amount of \$
- ☒ The Commissioner is hereby authorized to credit overpayments or charge the following fees associated with this communication to Deposit Account No. 50-0220:
- a. ☒ Additional filing fees under 37 CFR 1.16 for presentation of extra claims.
 - b. ☒ Additional patent application processing fees under 37 CFR 1.17.


Respectfully submitted,


Elizabeth A. Stanek
Registration No.

CERTIFICATE OF EXPRESS MAILING

Express Mail Label No. EV 318419683 US
Date of Deposit: November 12, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated above and is addressed to: Mail Stop PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450


Rosa Lee Brinson

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hyoung-Joon Kim *et al.*

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: METHODS OF FABRICATING INTEGRATED CIRCUIT DEVCIES PROVIDING
IMPROVED SHORT PREVENTION

Date: November 12, 2003

MS PATENT APPLICATION

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

REMARKS

Claims 1-12 correspond to Claims 19-30 of the parent application. Favorable examination and allowance of the present divisional application is respectfully requested.

Please note a copy of the certified Korean Application was submitted with the parent application. However, a duplicate copy of the Korean application will be submitted upon request.

Respectfully submitted,



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Rosa Lee Brinson